T Number	II i to	Search Text	DB	Timo stamo	
L Number	Hits			Time stamp	16.55
1	2	6357004.pn.,6507904.pn.	USPAT	2003/10/28	
-	47	("6507904"	USPAT	2003/10/22	14:04
1		"6633963"			
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İ		"6148387"			
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-	2	6507904.PN.,6633963.PN.	USPAT	2003/10/21	
-	3686	713/200,201,155-156,164-168,170-176,180,182	USSPACICLS.	2003/10/21	
-	0	380/30,259-266.CCLS	USPAT	2003/10/21	18:31
-	1324	380/30,259-266.CCLS.	USPAT	2003/10/21	18:31
-	274	340/545.1.CCLS.	USPAT	2003/10/21	
-	2073	711/163,153,152,170,173.CCLS.	USPAT	2003/10/21	
-	28	k5 and isolat\$4 same memory	USPAT	2003/10/21	
_	3888	713/200, 201, 155-156, 164-168, 170-176, 180, 182		2003/10/21	
		and chipset or chip-set or "chip set"			
_	65	713/200, 201, 155-156, 164-168, 170-176, 180, 182	119S4PACTCT.S	2003/10/21	18:47
	"	and (chipset or chip-set or "chip set")	, 2011-340110.		10.11
<u> </u>	0	(713/200,201,155-156,164-168,170-176,180,18	DITE OD ANY OF C	2003/10/21	18.47
-	l o		Chrawicory.	2003/10/21	10.47
		and (chipset or chip-set or "chip set"))			
	_	and (isolat\$ adj execut\$4)	11/0407-0007-0	2002/12/21	10.47
-	2	713/200, 201, 155-156, 164-168, 170-176, 180, 182	DSFACCLS.	2003/10/21	18:47
		and (isolat\$ adj execut\$4)			
-	10	380/30,259-266.CCLS. and (chipset or	USPAT	2003/10/21	18:49
		chip-set or "chip set")			
-	55	711/163,153,152,170,173.CCLS. and (chipset	USPAT	2003/10/21	18:52
		or chip-set or "chip set")			
	·				

-	3	(711/163,153,152,170,173.CCLS. and (chipset or chip-set or "chip set")) and	USPAT	2003/10/21 18:52
-	0	<pre>(isolat\$5 with memory) 340/545.1.CCLS. and (chipset or chip-set or "chip set")</pre>	USPAT	2003/10/21 18:52
-	0	interface and decoder and "chipset storage" and "communication storage"	USPAT	2003/10/22 14:07
_	0	"processor nub loader hash"	USPAT	2003/10/22 14:08
-	0	(token adj bus) and "ICH interface bus"	USPAT;	2003/10/22 14:09
			EPO; DERWENT;	
-	36	ICH and chipset	IBM_TDB USPAT; EPO;	2003/10/22 14:21
	35	(ICH and chipset) and interface and device	DERWENT; IBM_TDB USPAT;	2003/10/22 14:21
-	35	(ICH and Chipset) and interface and device	EPO; DERWENT;	2003/10/22 14:21
			IBM TDB	
-	27	((ICH and chipset) and interface and device) and address and processor	USPĀT; EPO;	2003/10/22 14:21
		device, and address and processor	DERWENT;	
_	150595	((memory or storage) with address)	IBM_TDB USPAT;	2003/10/22 14:22
			EPO;	
			DERWENT; IBM TDB	
-	9	(((ICH and chipset) and interface and	USPĀT;	2003/10/22 14:22
		device) and address and processor) and ((memory or storage) with address)	EPO; DERWENT;	
		((memory or storage) with address)	IBM TDB	
_	0	(((ICH and chipset) and interface and	USPĀT;	2003/10/22 14:23
		device) and address and processor) and (secur\$5 with information)	EPO; DERWENT;	
	•	(Seedly 3 with Intolmation)	IBM_TDB	
-	11	(((ICH and chipset) and interface and device) and address and processor) and	USPĀT; EPO;	2003/10/22 14:26
		exchange\$4	DERWENT;	
	1.0		IBM_TDB	0000 /10 /00 14 04
1 -	10	(((ICH and chipset) and interface and device) and address and processor) and	USPAT; EPO;	2003/10/22 14:34
		key\$1	DERWENT;	
_	0	(ICH and chipset) and attestation	IBM_TDB USPAT;	2003/10/22 14:34
		, 1231 and dispose, and decoded ton	EPO;	
			DERWENT; IBM TDB	
_	2	(((ICH and chipset) and interface and	USPAT;	2003/10/22 14:35
		device) and address and processor) and	EPO;	
		(execution adj mode)	DERWENT; IBM TDB	
-	12	1 ' ' '	USPĀT;	2003/10/22 15:00
		device) and address and processor) and decod\$5	EPO; DERWENT;	
			IBM_TDB	
-	710	public adj key and key adj certificat\$5	USPAT; EPO;	2003/10/22 15:00
			DERWENT;	
			IBM TDB	0000/10/00 15 15
-	23	(public adj key and key adj certificat\$5) and chipset	USPAT; EPO;	2003/10/22 15:10
		3	DERWENT;	
_	1083	processor and device and his and chinest	IBM_TDB USPAT;	2003/10/22 16:47
-	1083	processor and device and bus and chipset	EPO;	2003/10/22 10:4/
			DERWENT;	
_	34	(processor and device and bus and chipset)	IBM_TDB USPAT;	2003/10/22 17:31
		and key and certificat\$4	EPO;	
			DERWENT; IBM TDB	
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	122	nonce	USPAT	2003/10/28 11:37
_	2	hash adj log	USPAT	2003/10/28 11:39
_	اً وَ	nub adj loader adj hash	USPAT	2003/10/28 11:37
<u>-</u>	1818	chipset withstor\$5 with hash with log	USPAT	2003/10/28 11:40
_	0	chipset with stor\$5 with hash with log	USPAT	2003/10/28 11:40
l _	Ιŏ	chipset with store with hash with log	USPAT	2003/10/28 11:40
l <u></u>	156	1 -	USPAT	2003/10/28 11:41
_	1 10	chipset with stor\$5 with hash	USPAT	2003/10/28 11:41
l <u> </u>	ا آ	chipset with stor\$5 and hash	USPAT	2003/10/28 12:16
l _	156	l •	USPAT	2003/10/28 12:16
ł _	13	chipset adj stor\$	USPAT	2003/10/28 12:21
<u>-</u>	1 1	6357004.pn. and chipset	USPAT	2003/10/28 12:23
-	34	chipset and (hash adj function)	USPAT	2003/10/28 12:26
	19	chipset and hash and log	USPAT	2003/10/28 12:20
-	0	"log in chipset"	USPAT	2003/10/28 12:31
. -	1		USPAT	2003/10/28 12:37
-	1 1	chipset with hash		
-	42	"chipset comprising"	USPAT	2003/10/28 12:39
-	0	"chipset comprising" and hash and log	USPAT	2003/10/28 12:39
	2	"chipset comprising" and hash	USPAT	2003/10/28 15:54

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